

IN THE UNITED STATES PATENT OFFICE

Applicant: Varadarajan Srinivasan et al
Assignee: NetLogic Microsystems, Inc.
Title: "Rollover Bits For Packet Departure Time Calculator"
Ser. No.: 10/613,776 Filing Date: 07/02/2003
Examiner: Sai Ming Chan Art Unit: 2609
Docket No.: NLMI.P213 Conf. No.: 6968

Mail Stop Amendment
COMMISSIONER FOR PATENTS
P.O Box 1450
Alexandria, VA 22313-1450

May 3, 2007

RESPONSE TO THE SECOND OFFICE ACTION

Dear Sir:

This paper is in response to the Second Office Action mailed from the Patent Office on April 6, 2007.

A Listing of the Claims begin on page 2 of this paper.

Remarks begin on page 5 of this paper.